

10532993 - GAU: 2817



SHEET 1 OF 1

INFORMATION DISCLOSURE  
CITATION IN AN  
APPLICATIONATTY. DOCKET NO.  
**061352-0100**SERIAL NO.  
**10/532,993**

(PTO-1449)

APPLICANT  
**Michihito UEDA**FILING DATE  
**April 28, 2005**GROUP  
**Not yet assigned**

## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			

## FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number 4-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
		JP 48-102557 A	12/22/1973			partial only	

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		YAMANAKA et al.; "A CMOS Stochastic Associative Processor Using PWM Chaotic Signals"; <u>IEICE Trans. Electron.</u> ; c. 2001; Vol. E84, No. 12; pp. 1723 - 1729

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.